

PID	PID Description	SBEC Application			JTEC Application		
		Supported	Comp ID	Scaling (bit wt.)	Supported	Comp ID	Scaling (bit wt.)
00	Test PIDs Supported (\$01 - \$20)	X	FF	---	X	FF	---
20	Test PIDs Supported (\$21 - \$40)	X	FF	---	X	FF	---
40	Test PIDs Supported (\$41 - \$60)	X	FF	---	X	FF	---
60	Test PIDs Supported (\$61 - \$80)	X	FF	---	X	FF	---
O2 SENSOR MONITOR							
11	O2 Sensor 1/1 Half-Cycle Counter	X	80	1 count per bit	X	81	1 count per bit
13	O2 Sensor 1/1 Big-Slope Counter	X	80	1 count per bit	X	81	1 count per bit
31	O2 Sensor 2/1 Half-Cycle Counter	Dual-bank	80	1 count per bit	Dual-bank	81	1 count per bit
33	O2 Sensor 2/1 Big-Slope Counter	Dual-bank	80	1 count per bit	Dual-bank	81	1 count per bit
O2 HEATER MONITOR							
19	O2 Heater 1/1 Hot Trend Counter	X	80	1 count/bit	---	---	---
"	O2 Heater 1/1 Time to Reach Voltage	---	---	---	X	01	11 ms/bit
1A	O2 Heater 1/2 Hot Trend Counter	X	80	1 count/bit	---	---	---
"	O2 Heater 1/2 Time to Reach Voltage	---	---	---	X	01	11 ms/bit
1B	O2 Heater 1/1 Δ Voltage	X	80	20 mv/bit	---	---	---
"	O2 Heater 1/2 Time (JTEC, MD 8.0L truck)	---	---	---	O	01	11 ms/bit
1C	O2 Heater 1/2 Δ Voltage	X	80	20 mv/bit	---	---	---
"	O2 Heater 1/3 Time (JTEC, MD 8.0L truck)	---	---	---	O	01	11 ms/bit
39	O2 Heater 2/1 Hot Trend Counter	O	80	1 count/bit	---	---	---
"	O2 Heater 2/1 Time to Reach Voltage	---	---	---	O	01	11 ms/bit
3A	O2 Heater 2/2 Hot Trend Counter	O	80	1 count/bit	---	---	---
"	O2 Heater 2/2 Time to Reach Voltage	---	---	---	O	01	11 ms/bit
3B	O2 Heater 2/1 Δ Voltage	O	80	20 mv/bit	---	---	---
3C	O2 Heater 2/2 Δ Voltage	O	80	20 mv/bit	---	---	---
CATALYST SYSTEM MONITOR							
15	Catalyst Slow O2 1/1 Response Counter	X	80	1 count/bit	---	---	---
"	Catalyst 1/1 Phase Response Counter	---	---	---	X	81	1 count/bit
21	Catalyst 1/1 Switch Frequency Ratio	X	00	0.39% per bit	X	01	0.39% per bit
22	Catalyst 2/1 Switch Frequency Ratio	Dual-bank	00	0.39% per bit	Dual-bank	01	0.39% per bit
35	Catalyst Slow O2 2/1 Response Counter	X	80	1 count/bit	---	---	---
EGR SYSTEM MONITOR							
41	EGR Monitor Δ Adaptive Fuel Shift	X	80	0.19%/bit	---	---	---
"	EGR Monitor Fail Counter	---	---	---	X	01	1 count/bit
PURGE FLOW SYSTEM MONITOR							
51	Purge Monitor Δ IAC Shift	X	80	1 step per bit	X	81	1 step per bit
52	Purge Monitor Δ Adaptive Fuel Shift	X	80	0.19% per bit	X	81	0.39% per bit (use LSB only)
53	Purge Monitor Δ RPM Shift	X	80	1/8 RPM per bit	X	81	1/8 RPM per bit
EVAPORATIVE SYSTEM MONITOR							
61	0.040" Leak Detection Pump Period	X	80	100 ms per bit	X	81	11 ms per bit

NOTE: An 'X' in the column for SBEC, or JTEC, Engine applications indicates that this PID is supported for all unleaded gasoline engine applications. An 'O' indicates that this PID is application-specific and applies only to certain packages such as dual-bank O₂ systems, dual-bank catalyst systems, etc. A '---' indicates that this PID is not supported by this application. The component ID of \$00 for SBEC, or \$01 for JTEC represents a maximum test limit. For a minimum test limit, the component ID will be either \$80 for SBEC, or the component ID will be \$81 for JTEC.